

FIG 1

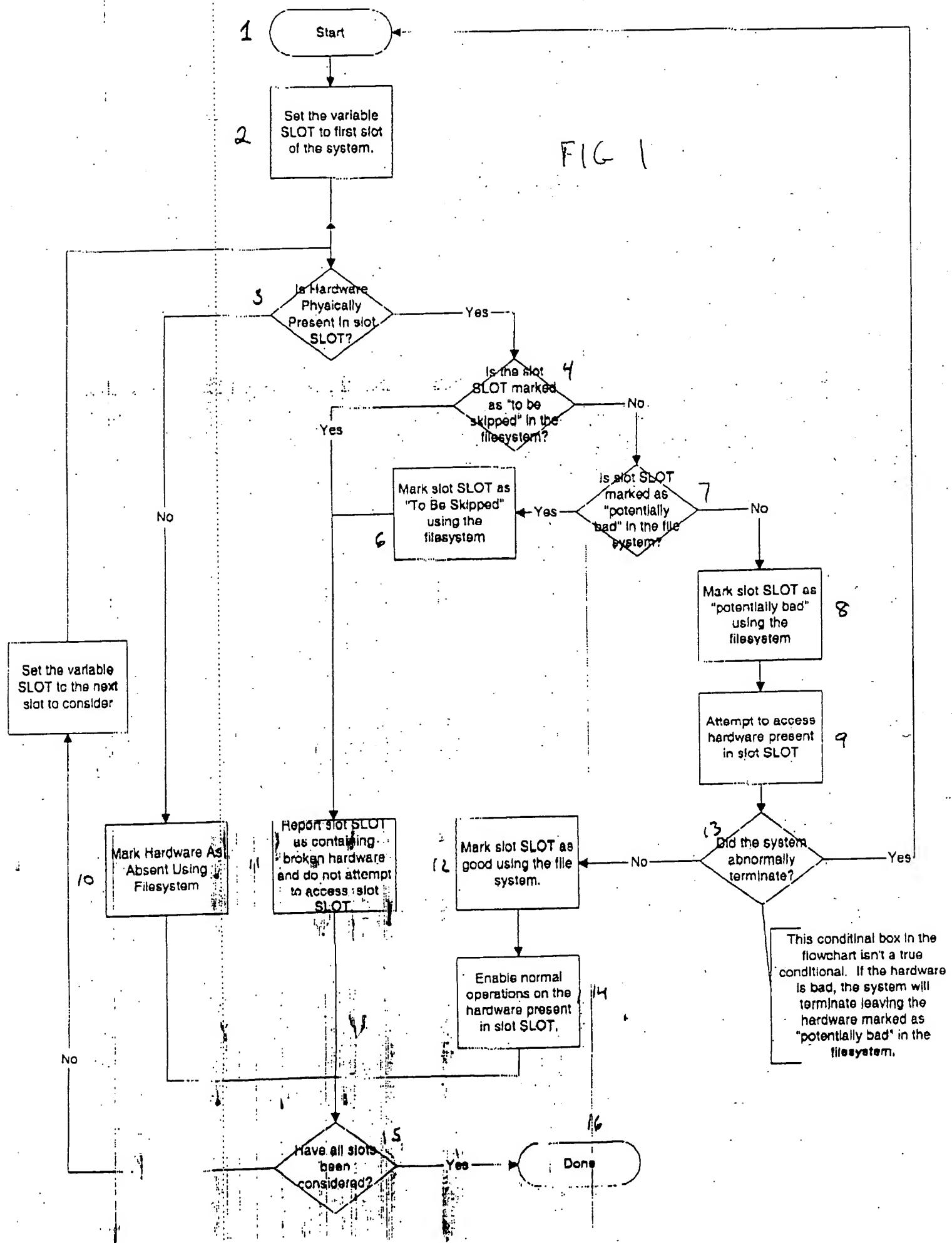


FIG 2

Switch - 10

Master Slave Bus - 16

Slots for bus slave units - 26

| | | | | |
|----------------------------|----------------------------|-----|----------------------------|----------------------------|
| bus slave unit 14 | bus slave unit 14 | ... | bus slave unit 14 | bus slave unit 14 |
|----------------------------|----------------------------|-----|----------------------------|----------------------------|

Control Processor - 28

Bus Master Unit - 12

Memory - 8

Software Program - 22

Variable - 24

Watchdog - 27

Persistent Storage - 28